



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Ruolin Li et al.

Serial No.: 10/848,924

Filed: May 19, 2004

For: Integrating Optical Components  
on a Planar Light Circuit

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Art Unit: 2874

Examiner: Phan T.H. Palmer

Docket: ITL.1077US  
P18308

Assignee: Intel Corporation

Mail Stop **Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO PAPER NO. 0905**

Sir:

In response to the office action mailed September 7, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: November 17, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Cynthia L. Hayden*  
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